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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/512,077	12/09/2004	Olivier Marty	04202	2792
23318 7591 DENNISON, SCHULTZ & MACDONALD 1727 KING STREET			EXAMINER	
			RAO, G NAGESH	
SUITE 105 ALEXANDRI	A. VA 22314		ART UNIT	PAPER NUMBER
	,		1792	
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			02/11/2008	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

### Application No. Applicant(s) 10/512.077 MARTY ET AL. Office Action Summary Examiner Art Unit G. NAGESH RAO 1792 -- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --Period for Reply A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS. WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION. Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b). Status 1) Responsive to communication(s) filed on 11/19/07. 2a) This action is FINAL. 2b) This action is non-final. 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213. Disposition of Claims 4) Claim(s) 17-27 is/are pending in the application. 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration. 5) Claim(s) \_\_\_\_\_ is/are allowed. 6) Claim(s) 17-27 is/are rejected. 7) Claim(s) \_\_\_\_\_ is/are objected to. 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement. Application Papers 9) The specification is objected to by the Examiner. 10) ☐ The drawing(s) filed on 05 November 2004 is/are: a) ☐ accepted or b) ☐ objected to by the Examiner. Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a). Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d). 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152. Priority under 35 U.S.C. § 119 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some \* c) None of: Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). \* See the attached detailed Office action for a list of the certified copies not received. Attachment(s) 1) Notice of References Cited (PTO-892) 4) Interview Summary (PTO-413) Paper No(s)/Mail Date. Notice of Draftsperson's Patent Drawing Review (PTO-948) Notice of Informal Patent Application

Imformation Disclosure Statement(s) (PTC/S5/08)
 Paper No(s)/Mail Date \_\_\_\_\_\_.

6) Other:

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#### Continued Examination Under 37 CFR 1.114

1) A request for continued examination under 37 CFR 1.1 14, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.1 14, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 11/19/07 has been entered.

## Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- Claims 17-24 are rejected under 35 U.S.C. 102(b) as being anticipated by Yoshida (JP02000091627A).

Examiner has enclosed a machine translation of Yoshida '627 for applicant's review and consideration. Yoshida '627 pertains to the fabrication of a semiconductor LED whereby the substrate and thin layer formed atop are in the nanoscopic scale, thus defining them as nanostructured materials and providing for their utilization in a field such as microelectronics or nanotechnology. Whereby the

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thin layer is disposed on the substrate via an epitaxial deposition (such as MBE or MOCVD), followed by the radiation heat treatment which in part affects the substrate in order to aid in the heteroepitaxial adherence between the two materials, forcing the substrate to incur internal dislocations as a result of the treatment which in turn would effect the thin film but ultimately allow for the two materials to adhere much more strongly to one another. Furthermore as can be denoted by Figures A and B, an intermediate thin layer can be disposed between the substrate and thin layer (See English Translation enclosed for further details).

## Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

The factual inquiries set forth in *Graham v. John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:

- Determining the scope and contents of the prior art.
- Ascertaining the differences between the prior art and the claims at issue.
- 3. Resolving the level of ordinary skill in the pertinent art.

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 Considering objective evidence present in the application indicating obviousness or nonobviousness.

This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary.

Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

Claims 25-27 are rejected under 35 U.S.C. 103(a) as being unpatentable over
 Yoshida (JP02000091627A) in view of Pechenik (US Patent No. 6,365,059).

Yoshida 627 as mentioned before described processes for making a heteroepitaxial layered substrate based material at the nano-scale.

However Yoshida 627 failed to teach a lithographic operation to reveal piezoelectric lines or the very least the use of piezoelectric materials incorporated into the process of making the nano-based structured device.

In analogous art pertaining to the manufacturing of nano-based structures,

Pechenik 059 teaches the use of methods pertaining to the manufacturing of nano-

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scale electronic devices. Whereby it is well known to use nano-lithograpy in etching out portions of material to create patterns for building ground of fabrication of high end circuitry and dielectrics. As well it is well known to employ the use of piezoelectric thin films in these nano-based devices in order to create a more useful surface attaching environment to the thin film structure (See Col 1 Lines 1-68 – Col 2 Lines 1-68, Col 5 Lines 1-11, Col 6 Lines 59-68, and Col 7 Lines 1-18).

It would therefore been obvious to one having ordinary skill in the art at the time of the invention to enhance the teachings of Yoshida 627 with that of Pechenik 059, in the hopes of creating a much more highly efficient nano-based device (See Col 7 Lines 5-10).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to G. NAGESH RAO whose telephone number is (571)272-2946. The examiner can normally be reached on 9AM-5PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael Barr can be reached on (571)272-1414. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

GNR

/Robert M Kunemund/ Primary Examiner, Art Unit 1792